Amendments to the Claims:

1. (currently amended) A method to write in flash type memory of an electronic module comprising:

   associating simultaneously and permanently at least two physical areas of said memory, called mirror areas, with a the same logical area for storing a content;

   designating one of the physical areas as being an active physical area; and

   during a write in to said logical area, in programming the content of said logical area into in one of said blank mirror areas, called the active physical area.

2. (currently amended) The method according to claim 1, further comprising:

   erasing the content of all mirror areas used in a single operation at a convenient time.

3. (currently amended) The method according to claim 2, comprising performing the erasure during wherein the convenient time is a period of inactivity or when all the mirror physical areas are used.

4. (currently amended) The method according to one of claims 1 to 3, comprising copying the active physical area into a buffer area, erasing all mirror physical areas and copying the buffer into the a first area available.

5. (currently amended) The method according to one of claims 12 to 3, comprising performing the erasure and programming/read operations in parallel without thereby not blocking the electronic modulesystem.

6. (currently amended) The method according to claim 5, comprising:

performing the erasure and programming/read operations in parallel in a bi-bank memory, each bank having mirror area(s), one bank being used for programming/reading while the other bank is erased, ~~the method~~

changing active bank when all mirror areas of the bank used for programming/read have been used.

7. (currently amended) The method according to ~~one of claims 1 to 6~~ claim 1, 2, or 3, comprising designating said active physical areas using a counter and incrementing the counter ~~incremented~~ on each change of active area.

8. (currently amended) The method according to ~~one of claims 1 to 7~~ claim 1, 2 or 3, comprising associating at least one bit with a logical area to represent ~~representing~~ the use state of at least one mirror physical area of said logical area.

9. (currently amended) The method according to ~~one of claims 1 to 7~~ claim 1, 2, or 3, wherein if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure, the write is carried out in an active physical area ~~if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure~~, and in a blank physical area otherwise.

10. (currently amended) The method according to claim 9, ~~characterized~~ comprising programming only ~~part~~ a portion of the logical area in the blank physical area.

11. (currently amended) An electronic module having information processing means and comprising a flash type non volatile memory having a mirror memory formed from at least two physical areas ~~and~~

simultaneously and permanently associated with the same logical area, each new programming operation ~~in~~ to said logical area taking place in an area of the ~~blank~~ mirror ~~area~~memory.

12. (currently amended) A card ~~characterized in that it includes~~ comprising an electronic module having information processing means and ~~comprising~~ a flash type non volatile memory having a mirror memory formed from at least two physical areas ~~and~~ simultaneously and permanently associated with ~~the~~ a same logical area, each new programming operation ~~in~~ to said logical area taking place in an area of the ~~blank~~ mirror ~~area~~ memory.

13. (currently amended) A computer program comprising program code instructions to cause a microprocessor to write in a flash type memory of an electronic module, wherein the computer program instructions comprise ~~instruction~~ instructions for

simultaneously and permanently associating at least two physical areas of said memory, called mirror areas, with ~~the~~ a same logical area for storing a content,

designating one of the physical areas as being an active physical area, and

during a write in said logical area, ~~in~~ programming the content of said logical area in ~~one of said blank mirror areas,~~ ~~called~~ the active physical area.

14. (currently amended) The method according to claim 5, comprising designating said active physical areas using a counter and incrementing the counter ~~incremented~~ on each change of active area.

15. (currently amended) The method according to claim 6, comprising designating said active physical areas using a counter <u>and incrementing the counter</u> ~~incremented~~ on each change of active area.

16. (currently amended) The method according to claim 5, comprising associating at least one bit with a logical area <u>to represent</u> ~~representing~~ the use state of at least one mirror physical area of said logical area.

17. (currently amended) The method according to claim 6, comprising associating at least one bit with a logical area <u>to represent</u> ~~representing~~ the use state of at least one mirror physical area of said logical area.

18. (currently amended) The method according to claim 7, comprising associating at least one bit with a logical area <u>to represent</u> ~~representing~~ the use state of at least one mirror physical area of said logical area.

19. (currently amended) The method according to claim 5, wherein the write is carried out in an active physical area <u>if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure</u>, the write is carried out in an active physical area ~~if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure~~, and in a blank physical area otherwise.

20. (currently amended) The method according to claim 6, wherein the write is carried out in an active physical area <u>if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure</u>, the write is carried out in an active physical area ~~if the content of the logical area is identical to the~~

~~content of the active physical area or when said write involves no erasure~~, and in a blank physical area otherwise.

21. (currently amended) The method according to claim 7, wherein the write is carried out in an active physical area <u>if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure</u>, the write is carried out in an active physical area ~~if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure~~, and in a blank physical area otherwise.

22. (currently amended) The method according to claim 19, comprising programming only ~~part~~ <u>a portion</u> of the logical area in the blank physical area.

23. (previously presented) The computer program of claim 13, wherein the computer program instructions further comprise instructions to erase the content of all mirror areas used in a single operation at a convenient time.

24. (currently amended) The computer program of claim 23, wherein ~~the computer program instructions further comprise instructions to, when erasing the content of all mirror areas used in a single operation at~~ <u>the</u> convenient time ~~is~~<u>,</u> ~~performing the erasure during~~ a period of inactivity or when all the mirror physical areas are used.

25. (currently amended) The computer program of claim 13, 23, or 24 wherein the computer program instructions further comprise instructions to copy the active physical area into a buffer area, erasing all mirror physical areas, and copying the buffer into ~~the~~ <u>a</u> first area available.

26. (currently amended) The computer program of claim 13, 23, or 24 wherein the computer program instructions further comprise instructions to perform the erasure and programming/read operations in parallel without blocking the ~~system~~ electronic module.

27. (previously presented) The computer program of claim 26, wherein the computer program instructions further comprise instructions to perform the erasure and programming/read operations in parallel in a bi-bank memory, each bank having mirror area(s), one bank being used for programming/reading while the other bank is erased, the method changing active bank when all mirror areas of the bank used for programming/read have been used.

28. (currently amended) The computer program of claim 13, 23, or ~~26~~ 24 wherein the computer program instructions further comprise instructions to designate said active physical areas using a counter incremented on each change of active area.

29. (previously presented) The computer program of claim 13, 23, or 24 wherein the computer program instructions further comprise instructions to associate at least one bit with a logical area representing the use state of at least one mirror physical area of said logical area.

30. (currently amended) The computer program of claim 13, 23, or 24 wherein the computer program instructions further comprise instructions wherein the write is carried out in an active physical area if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure, ~~and~~ otherwise in a blank physical area that becomes the active physical area~~otherwise~~.

31. (currently amended) The computer program of claim ~~13, 23, or 24~~ <u>30</u> wherein the computer program instructions further comprise instructions to program only part of the logical area in the blank physical area.

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